

Fast charge sensing of a cavity-coupled double quantum dot using a Josephson parametric amplifier

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We demonstrate fast readout of a double quantum dot (DQD) that is coupled to a superconducting resonator. Utilizing parametric amplification in a nonlinear operational mode, we improve the signal-to-noise ratio (SNR) by a factor of 2000 compared to the situation with the parametric amplifier turned off. With an integration time of 400 ns we achieve a SNR of 76. By studying SNR as a function of the integration time we extract an equivalent charge sensitivity of $8 \times 10^{-5} \text{ e}/\sqrt{\text{Hz}}$. The high SNR allows us to acquire a DQD charge stability diagram in just 20 ms. At such a high data rate, it is possible to acquire charge stability diagrams in a live “video-mode,” enabling real time tuning of the DQD confinement potential.

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I. INTRODUCTION

Conventional charge transport experiments directly measure the electrical conductance or current flow through a sample. Instead of measuring the rate at which electrons flow through a sample, it is sometimes desirable to have direct access to the charge degree of freedom using sensitive electrometers. For example, charge sensing can be used for real-time counting of electrons [1], imaging of current flow through nanostructures such as quantum point contacts [2], imaging electron-hole puddles in graphene [3], and single electron capacitance spectroscopy of quantum Hall states [4, 5]. Charge detection is also a necessary ingredient for quantum computation using electrons trapped in gate defined quantum dots [6, 7]. For spin qubits, a process called spin-to-charge conversion is used for readout [8, 9]. Charge sensing is also needed to accurately measure the charge stability diagram of double quantum dots (DQD) and determine the absolute electronic occupancy. Sensing in quantum dots has traditionally been performed using quantum point contacts (QPC) or single electron transistors (SET) [10–12].

One method to improve the charge sensing bandwidth involves embedding the QPC or SET in a resonant tank circuit and performing rf-reflectometry [13–15]. Reflectometry experiments have so far utilized standard cryogenic low-noise amplifiers [16]. The increased data acquisition rate of rf-based measurements reduces the susceptibility to $1/f$ noise and enables single-shot spin readout [17].

While rf-reflectometry has allowed charge detection times as short as 100 ns with a signal-to-noise ratio (SNR) ~ 10 [18, 19], recent progress in superconducting quantum circuits opens a path forward for further improvements. The application of quantum limited Josephson parametric amplifiers (JPAs) [20–26] has accelerated progress in the field of superconducting qubits. These

amplifiers are nearly quantum limited, meaning they add the minimum amount of noise dictated by the uncertainty relation [22, 27, 28], and have enabled high fidelity single shot readout of qubit states [29] as well as squeezed light experiments [22, 30, 31].

In this paper we demonstrate JPA-assisted readout of a DQD that is coupled to a high frequency superconducting cavity. We achieve a factor of 2000 improvement in the SNR by operating the JPA in a nonlinear regime, in which the average readout signal experiences a larger amplification than the incoming noise. With an integration time of just 400 ns, we achieve a SNR of 76. The large SNR allows us to efficiently map out the DQD charge stability diagram in just 20 ms using a dual gate-voltage rastering scheme. Such dramatic improvements enable real-time “video-mode” tuning of a DQD device. The faster device optimization that JPA assisted readout allows will become crucial as quantum dot architectures with more control parameters are developed [32–34].

II. JOSEPHSON PARAMETRIC AMPLIFIER ASSISTED READOUT OF A CAVITY-COUPLED SEMICONDUCTOR DOUBLE QUANTUM DOT

Our device architecture is schematically illustrated in Fig. 1(a). The experiment consists of three main components: a DQD formed in an InAs nanowire, a Nb superconducting cavity to which the DQD is coupled, and a JPA that is used to amplify the output field of the cavity. In the following sections we describe each of these components in detail.

A. InAs Double Quantum Dot

The DQD is fabricated using a bottom-gated architecture as shown in Fig. 1(b) [35]. We place a single InAs nanowire of nominally 50 nm diameter [36] across five

depletion gates [37]. We label the gates V_{LW} , V_L , V_M , V_R , and V_{RW} . A double-well potential is formed by selectively depleting the nanowire using the bottom gates [37]. Here V_{LW} and V_{RW} control the left and right barriers, V_L and V_R control μ_L and μ_R , the electrochemical potential of each dot, and V_M controls the interdot tunneling rate [7]. After passivating the nanowire using an ammonium polysulfide etch [38] we deposit Ti/Au contacts to form the source and drain electrodes. The source is connected to a voltage anti-node of a half-wavelength superconducting resonator and the drain is connected to the resonator ground plane [39].

B. Superconducting Cavity

The cavity is implemented as a coplanar half-wavelength superconducting resonator with a resonance frequency $f_c = 7881$ MHz, see Fig. 1(b). A 50 nm thick Nb film is sputter deposited on a high resistivity silicon substrate with a 250 nm thick layer of thermally grown SiO_2 . The input and output capacitors result in input and output coupling rates $\kappa_{\text{in}}/2\pi \approx \kappa_{\text{out}}/2\pi \approx 0.39$ MHz. Internal radiation losses result in a total cavity decay rate $\kappa_{\text{tot}}/2\pi = 2.6$ MHz equivalent to a loaded quality factor $Q_c \sim 3000$. The source contact of the nanowire is connected to a voltage anti-node of the resonator, resulting in a large electric dipole coupling between the DQD and the resonator [39–41]. We probe the hybrid system by applying a tone $f_{\text{in}} = f_c$ of power $P_{\text{in}} \approx -110$ dBm to the input port of the cavity. Charge dynamics in the DQD change the effective admittance of the microwave cavity leading to a measurable dispersive shift of the resonance frequency [39–41].

C. Josephson Parametric Amplifier

As shown in Fig. 1(c) the JPA is implemented as a quarter-wavelength resonator etched from a 200 nm Nb layer on top of a sapphire substrate [22, 31]. The input port has a coupling rate κ_{JPA} and is formed using an interdigitated capacitor geometry. The resonator is shunted by a series array of $M = 5$ superconducting quantum interference devices (SQUIDS). The SQUIDS act as a non-linear inductor and allow for amplification of the signal field via the pump field resulting in a Lorentzian gain profile with nearly constant gain-bandwidth product: $\sqrt{GB_w} \propto \kappa_{\text{JPA}}/2\pi$ [22]. Here G is the JPA power gain (which can be tuned using the applied pump power P_{pump}), while B_w is the 3 dB bandwidth. The dynamic range of the amplifier is set by the strength of the pump field required for a given gain and can be improved by increasing the number of SQUIDS [42]. We choose $M = 5$ with each SQUID having maximal Josephson energy $E_J/h = 7 \pm 0.5$ THz and de-

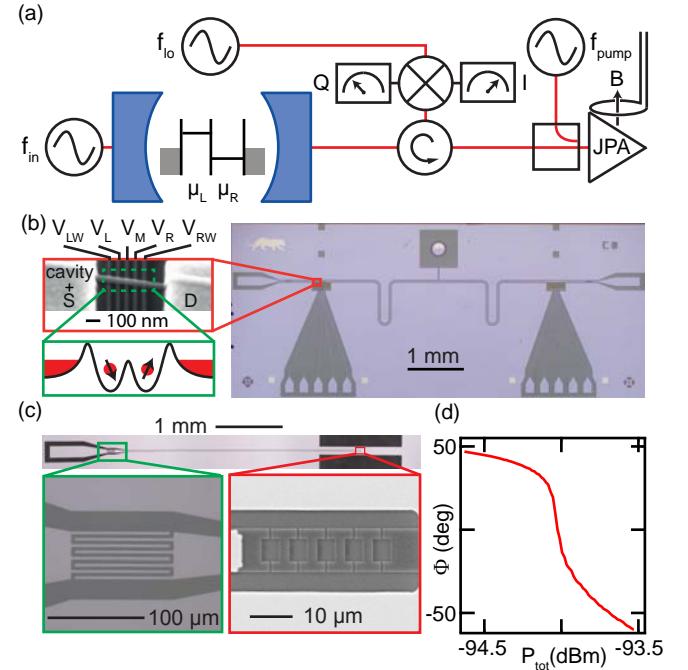


Figure 1. (Color online) (a) Diagram of the experimental setup. A tone f_{in} is used to probe a cavity-coupled DQD. The output field of the cavity is amplified using a JPA and then demodulated into the I and Q quadratures. The amplification band of the JPA is flux-tuned using a small coil that creates a magnetic field B . (b) Illustration of the DQD-cavity system. A single nanowire is placed on top of 5 depletion gates. Negative voltages on the gates establish a double-well potential, forming the DQD. (c) Optical micrograph of the JPA, which is implemented as a quarter-wavelength resonator shunted by an array of 5 SQUIDS. The resonator is coupled to the input port using an interdigitated planar capacitor (outlined in green). A scanning electron micrograph showing the SQUID array is outlined in red. (d) Phase Φ of the signal reflected off of the JPA as a function of the total incident power P_{tot} . We operate in phase-sensitive mode with $f_{\text{in}} = f_{\text{pump}}$ and use the strong phase dependence to translate small differences in the cavity output field to large phase shifts of the microwaves reflected off of the JPA.

sign our coupling capacitance to be 40 fF for a target $\kappa_{\text{JPA}}/2\pi \sim 100$ MHz. A detailed treatment of the design considerations of this class of amplifiers is given in Ref. [42].

We use the amplifier in phase-sensitive mode [22, 43] and set $f_{\text{pump}} = f_{\text{in}} = 7881$ MHz, which is the resonance frequency of our cavity. The JPA is tuned such that the reflected phase Φ is highly sensitive to the total incident power P_{tot} . Here P_{tot} is a combination of the pump power P_{pump} and the power of the cavity output field, P_{out} . Figure 1(d) shows Φ as a function of P_{tot} . We set $P_{\text{pump}} = -94$ dBm, which maximizes $d\Phi/dP_{\text{tot}}$. Adding the cavity output field to the pump modifies the total power incident on the JPA. This will result in large differences of the phase of the reflected pump signal. Thus in this measurement mode the small cavity output field

modulates the phase of the much stronger reflected pump tone, leading to amplification.

We further improve the SNR of our readout chain by driving the JPA beyond the regime where it amplifies linearly. We choose the power and the phase of the cavity drive field such that the dispersive shift changes the total power at the JPA from $P_{\text{tot}} \approx -93.9$ dBm to $P_{\text{tot}} \approx -94.1$ dBm [44]. As such, the dispersive shift will change P_{tot} from above the region of maximum $d\Phi/dP_{\text{tot}}$ to below. This means that while the dispersive shift produces a large change in Φ , any noise fluctuations will invariably occur in regions with smaller $d\Phi/dP_{\text{tot}}$ and be amplified less [44]. The bias point of the JPA is thus chosen such that its amplification characteristics fall between those of a linear amplifier and a bifurcation amplifier [23].

The signal exiting the JPA is further amplified using a high electron mobility transistor (HEMT) amplifier at the 3 K stage of the dilution refrigerator and room temperature amplifiers [44]. We can turn off the JPA by simply removing the pump tone. Here the signal from the cavity reflects off of the JPA with unity gain. In both cases we demodulate the signal using a heterodyne detection setup. We first downconvert the signal to 12.5 MHz by mixing it with a local oscillator tone $f_{\text{lo}} = f_{\text{in}} + 12.5$ MHz. It is then digitized using a FPGA-based digital homodyne stage to recover the I and Q quadrature amplitudes [44].

III. RESULTS

We now highlight the dramatic enhancement in the data acquisition rate that is enabled by the JPA. We first demonstrate rapid measurements of a large-scale DQD charge stability diagram using a JPA. The SNR values obtained with and without the JPA are then compared, indicating a factor of 2000 improvement with the JPA. Lastly, we demonstrate video-mode acquisition of charge stability diagrams using a rastered sweep of two gate voltages, resulting in data display that is similar to a cathode ray tube and of a comparable refresh rate.

A. Double Quantum Dot Charge Stability Diagram

A large scale charge stability diagram is shown in Fig. 2, where we plot the normalized Q quadrature amplitude as a function of V_L and V_R . These data are acquired by measuring the total reflected field from the JPA with the JPA pump tone on. With the DQD in Coulomb blockade, the resonator is insensitive to the DQD, resulting in large regions in the stability diagram with uniform amplitude and phase. Following previous work, we normalize the amplitude $A = I + iQ$, such that $|A| = 1$ and $\arg(A) = 1$ in these charge stability islands [39]. With this normalization the sensitivity of the Q quadrature to

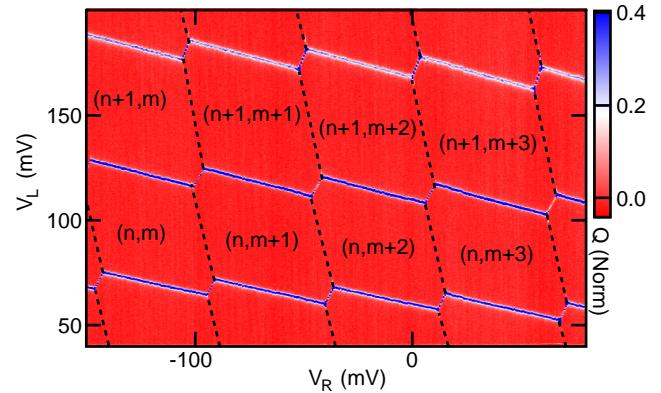


Figure 2. (Color online) DQD charge stability diagram extracted by plotting the normalized Q quadrature amplitude as a function of gate voltages V_L and V_R . The plot was taken with the JPA turned on and only took 2 minutes to acquire. Dashed lines are guides to the eye.

charge transitions is maximized and its mean value in Coulomb blockade is $\langle Q \rangle = 0$.

When an electron can tunnel between the two dots (interdot charge transition) or on and off one of the dots (single dot charge transition), the transmitted field through the resonator will be affected, which we detect as a non-zero Q quadrature amplitude. As a result the data in Fig. 2 trace out the charge stability diagram of the DQD [7]. The nearly horizontal lines are associated with the addition or removal of an electron from the left dot. The lines with positive slope correspond to interdot charge transitions where the total electron number is fixed, but an electron moves from one dot to the other. Due to the large distance between the cavity center pin and the right tunnel barrier, right dot charge transitions are not visible. We can infer their location based on the positions of the interdot charging lines. We use the charging lines to label the electron states as (n, m) , where n (m) is the number of electrons on the left (right) dot. For this data set, the device is configured in the many-electron regime, where $n \sim m \sim 20$. We note that the data shown in Fig. 2 were acquired in just 2 minutes. In comparison, acquiring similar quality data using standard low frequency conductance measurements of a quantum point contact would require approximately an hour.

B. Signal-to-Noise Measurements

To quantify the improvement in the SNR due to the JPA we investigate the sensing signal at an interdot charge transition. Interdot charge transitions are most relevant in quantum control experiments since interdot tunneling is spin selective and can be used to distinguish spin singlet and triplet states [9, 39, 45, 46]. Figures 3(a-b) show charge stability diagrams obtained near the $(n, m + 1) \leftrightarrow (n - 1, m + 2)$ interdot charge transition.

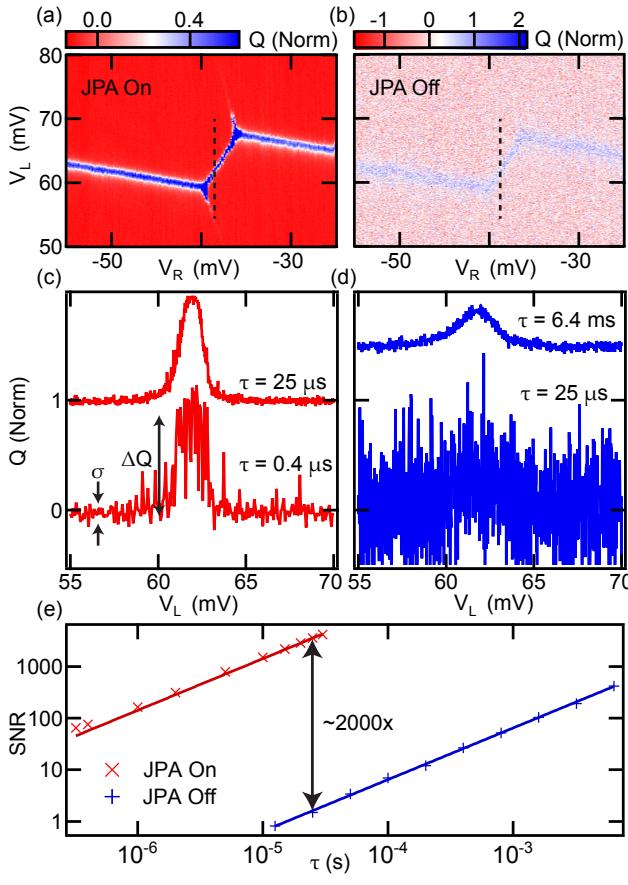


Figure 3. (Color online) (a–b) Q quadrature amplitude plotted as a function of V_L and V_R near the $(n, m+1) \leftrightarrow (n-1, m+2)$ interdot charge transition. (a) Data taken with the JPA on, and (b) with the JPA off. For both cases each point of the colormap is integrated for $\tau = 42 \mu\text{s}$. (c–d) Traces of the Q quadrature amplitude showing the interdot charge transition for different per-point integration times τ . (c) Data taken with the JPA on (traces are offset by 1 for clarity), and (d) with the JPA off (traces are offset by 1.5 for clarity). (e) Signal-to-noise ratio (SNR) for the interdot charge transition as a function of the integration time τ . Amplification with the JPA results in a factor of 2000 improvement in the SNR. Solid lines are fits to Eq. 2.

For each point of the colormap the Q quadrature is averaged for $\tau = 42 \mu\text{s}$. For a direct comparison, the data in Fig. 3(a) were acquired with the JPA turned on, while the data in Fig. 3(b) were acquired with the JPA turned off. The dramatic increase in the visibility of the charge transitions in Fig. 3(a) provides direct confirmation of increased SNR when the JPA is used. For a quantitative comparison, we ramp the gate voltage V_L along the dashed lines shown in Figs. 3(a–b) with a 50 Hz repetition rate and record time traces of the resultant Q quadrature amplitude. To study the SNR as a function of the effective per point integration time τ , we oversample at 12.5 MS/s and then apply an appropriately sized digital boxcar filter [44]. The results for $\tau = 400 \text{ ns}$ and $\tau = 25 \mu\text{s}$

with the JPA turned on are shown in Fig. 3(c). Even with $\tau = 400 \text{ ns}$ the interdot signal is clearly discernible. Following standard electronic references, we define the SNR as the ratio of the signal power to the noise power in the measured channel [47]. Using this definition,

$$\text{SNR} = \frac{\Delta Q^2}{\sigma^2}, \quad (1)$$

where ΔQ is the amplitude of the interdot charge transition signal and σ is the root-mean-square amplitude of the noise of the Q quadrature in Coulomb blockade [see Fig. 3(c)]. In the case of $\tau = 400 \text{ ns}$, we obtain a SNR ≈ 76 .

As illustrated by the data in Fig. 3(d), the SNR is much worse when only the HEMT is used. With $\tau = 25 \mu\text{s}$ the SNR = 1.4, meaning the charge transition is barely resolvable. The signal from the interdot charge transition can be recovered by increasing τ . Increasing τ beyond 500 μs is accomplished by averaging multiple time-traces [44]. The result for $\tau = 6.4 \text{ ms}$ is shown in the upper trace of Fig. 3(d).

The SNR is plotted as a function of the effective integration time τ in Fig. 3(e) with the JPA on and off. In both cases the curves are very well fit with

$$\text{SNR}(\tau) = \frac{\tau}{\tau_{\min}}. \quad (2)$$

Here τ_{\min} is a fitting parameter that corresponds to the minimum integration time required to achieve a $\text{SNR} = 1$. With the JPA on, a best fit is obtained with $\tau_{\min}^{\text{on}} = 7 \text{ ns}$, while with the JPA off $\tau_{\min}^{\text{off}} = 16 \mu\text{s}$, corresponding to a factor of 2000 improvement in the SNR. The linear dependence of the SNR on τ is expected and can be understood from the fact that τ sets the effective low-pass filter of the detection chain. Decreasing τ increases the bandwidth and therefore the noise power, while the signal power will remain constant [47].

We now estimate the minimum time required for single charge detection, in terms of both sensitivity and ultimate bandwidth. From the fitting described above the minimum integration time necessary for a $\text{SNR} = 1$ is $\tau_{\min}^{\text{on}} = 7 \text{ ns}$. Since charge sensitivity is related to minimum integration time through: $S = e\sqrt{\tau_{\min}}$, our detector has an equivalent charge sensitivity of $8 \times 10^{-5} \text{ e}/\sqrt{\text{Hz}}$ [17, 19]. The bandwidth of our detection chain is limited by the cavity linewidth and the JPA bandwidth. The former is set by $\kappa_{\text{tot}}/2\pi \approx 2.6 \text{ MHz}$ and can be extended by increasing the output coupling capacitance. The latter is constrained by the JPA gain-bandwidth product. In our case we operated the JPA with a gain $G \approx 4000$ and a bandwidth of 3.8 MHz [44]. This limit can be circumvented by more advanced amplifier designs [48, 49]. The above means that we are bandwidth limited by the cavity to detection times longer than $\approx 400 \text{ ns}$.

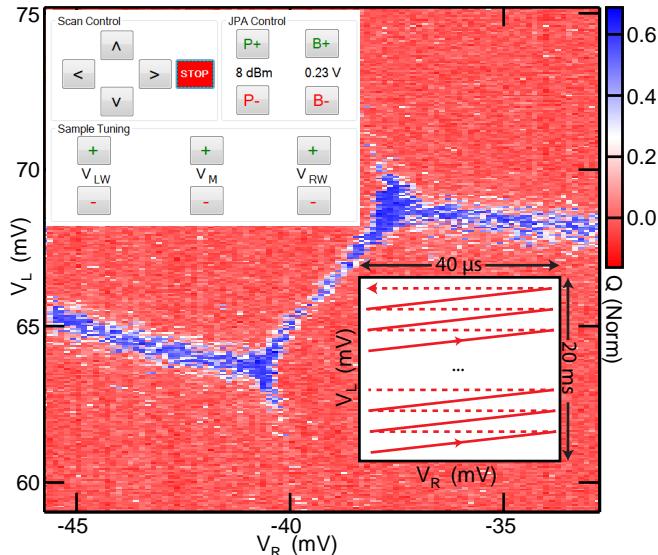


Figure 4. (Color online) Normalized Q quadrature amplitude as a function of V_L and V_R . The scan was acquired by rastering the gate voltages over a 20 ms period. The rastering scheme is illustrated in the lower inset. Starting in the lower left hand corner of the stability diagram, V_R is repeatedly ramped with a 40 μ s period. At the same time V_L is ramped up with a 20 ms period. The rastering configuration is similar to cathode-ray-tube monitors. The high SNR then allows for real-time tuning of the DQD. This is accomplished using an intuitive user interface pictured in the upper inset.

C. Video-Mode Data Acquisition

With the JPA on, the dramatic increase in the SNR allows for very fast acquisition of charge stability diagrams. For example with $\tau = 400$ ns a typical 100×100 point color map can be acquired in just 4 ms. To take advantage of this, we develop a dual gate-voltage rastering scheme. This is illustrated in the lower inset of Fig. 4. Starting in the lower left corner of the charge stability diagram, we repeatedly ramp V_R with a 40 μ s period. At the same time we also ramp V_L with a 20 ms period. The resulting data set rasters over a two-dimensional voltage subspace within a 20 ms period. During this time we record a single time trace of the measured quadrature with $\tau = 400$ ns and reorder it to form a 2D image that can be displayed. A sample frame acquired using this approach is shown in Fig. 4.

As coupled quantum dot systems continue to scale in complexity, it will become increasingly important to rapidly obtain charge stability diagrams and optimize the relevant tunneling rates. Traditionally, gate voltages have been optimized through a slow iterative process that involves changing gate voltages and observing the resulting charge stability diagram. The process is time consuming, in part due to the time necessary to acquire a new scan of the charge transition being tuned (often on

the order of minutes). We have built a simple user interface to control the experiment (see upper inset of Fig. 4) and use our rastering scheme to continuously update the charge transition in question. A user can then simply click on a set of buttons to adjust electrostatic gate voltages, while the results of the parameter tuning are displayed in real-time. This gives immediate feedback and allows one to arrive at the optimal gate voltages with ease. A demonstration of this form of device tuning is captured in a video in the supplementary materials [44].

IV. CONCLUSION AND OUTLOOK

In conclusion, we have used a JPA for fast measurements of a cavity-coupled semiconductor DQD. The addition of the JPA results in a factor of 2000 improvement in the SNR compared to traditional HEMT amplifier approaches. As a result, the minimum time to detect a single charge is limited by the linewidth of the cavity. We also develop a dual gate-voltage rastering scheme that allows us to measure a graph of a charge transition in 20 ms. This enables real-time device tuning, which has the potential to greatly increase the rate at which complex quantum dot devices are optimized. Together with frequency multiplexing [50, 51], JPA assisted readout may allow simultaneous fast readout of several DQDs.

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